dot, so there should always be a step at the start of the pulse. The fraction of traces in which this step is nevertheless missed, that is, $\Delta I_{\rm QPC}$ stays below the threshold (blue line in Fig. 3d), gives β_2 . We plot $1 - \beta_2$ in Fig. 4b (open dots). The resulting total fidelity for spin- \downarrow is given by $1 - \beta \approx (1 - \beta_1)(1 - \beta_2) + (\alpha\beta_1)$. The last term accounts for the case when a spin- \downarrow electron is flipped to spin- \uparrow , but there is nevertheless a step in $\Delta I_{\rm QPC}$ due to the dark-count mechanism. In Fig. 4b we also plot the extracted value of $1 - \beta$ as a function of the threshold.

We now choose the optimal value of the threshold as the one for which the visibility $1 - \alpha - \beta$ is maximal (red line in Fig. 4b). For this setting, $\alpha \approx 0.07$, $\beta_1 \approx 0.17$ and $\beta_2 \approx 0.15$, so the measurement fidelity for the spin- \uparrow and the spin- \downarrow state is ~0.93 and ~0.72, respectively. The measurement visibility in a single-shot measurement is thus at present 65%.

Significant improvements in the spin measurement visibility can be made by lowering the electron temperature (smaller α), and especially by making the charge measurement faster (smaller β). Already, the demonstration of single-shot spin read-out and the observation of T_1 of the order of 1 ms are encouraging results for the use of electron spins as quantum bits. Present experiments focus on measuring the phase coherence time, T_2 (by definition $\leq 2T_1$), by performing pulsed electron spin resonance experiments.

Received 6 April; accepted 25 May 2004; doi:10.1038/nature02693.

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Supplementary Information accompanies the paper on www.nature.com/nature.

Acknowledgements We thank D. P. DiVincenzo, H. A. Engel, T. Fujisawa, V. Golovach, Y. Hirayama, D. Loss, T. Saku, R. Schouten and S. Tarucha for technical support and discussions. This work was supported by the DARPA-QUIST programme, the ONR, the EU-RTN network on spintronics and the Dutch Organisation for Fundamental Research on Matter (FOM).

Competing interests statement The authors declare that they have no competing financial interests.

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Electrical detection of the spin resonance of a single electron in a silicon field-effect transistor

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The ability to manipulate and monitor a single-electron spin using electron spin resonance is a long-sought goal. Such control would be invaluable for nanoscopic spin electronics, quantum information processing using individual electron spin qubits and magnetic resonance imaging of single molecules. There have been several examples^{1,2} of magnetic resonance detection of a single-electron spin in solids. Spin resonance of a nitrogenvacancy defect centre in diamond has been detected optically³, and spin precession of a localized electron spin on a surface was detected^{4,5} using scanning tunnelling microscopy. Spins in semiconductors are particularly attractive for study because of their very long decoherence times⁶. Here we demonstrate electrical sensing of the magnetic resonance spin-flips of a single electron paramagnetic spin centre, formed by a defect in the gate oxide of a standard silicon transistor. The spin orientation is converted to electric charge, which we measure as a change in the source/drain channel current. Our set-up may facilitate the direct study of the physics of spin decoherence, and has the practical advantage of being composed of test transistors in a conventional, commercial, silicon integrated circuit. It is well known from the rich literature of magnetic resonance studies that there sometimes exist structural paramagnetic defects⁷ near the Si/SiO₂ interface. For a small transistor, there might be only one isolated trap state that is within a tunnelling distance of the channel, and that has a charging energy close to the Fermi level.

When a defect is present, the source/drain channel current can experience random telegraph signal (RTS), jumping between two discrete current values. These arise from two possible trapped electric charge states of the defect. The two charge states can correspond to the two spin orientations of a trapped electron. Field effect transistor (FET) current senses electrostatic charge (by definition), and can thus sense single-electron spin resonance.

In view of the high degree of perfection of silicon devices, 80% of the transistors that we tested actually had no such trap states at all. In those cases we applied a high voltage spike to the gate in an attempt to induce a paramagnetic defect for study. Electron spin in low-transition-temperature semiconductors is now recognized⁸ to have considerable potential for storing and manipulating quantum information. Some proposed qubit architectures trap electron spin in a controlled potential well⁹ at the Si–Ge interface¹⁰ and use transistor-like structures¹¹ for sensing.

Figure 1a sketches a simplified version of the transistor device in our experiment. The essential requirement for detecting a single spin flip is the conversion of spin-orientation to electric charge. In our approach, the Fermi level is adjusted so that it lies between the upper and lower Zeeman levels as illustrated in Fig. 1b. If the lower Zeeman level is occupied by one electron, as in Fig. 1b, it cannot accept any additional electrons from the Fermi level. If only the upper Zeeman level is occupied, as in Fig. 1c, then an additional electron can be transferred from the Fermi sea to the lower Zeeman level. The distinction between two trapped charges $2e^-$ (in Fig. 1c) versus one trapped charge $1e^-$ (in Fig. 1b) can be sensed, because an FET is basically an electrometer. Thus spin orientation is converted to electric charge, which is sensed by the source/drain current in an FET transistor.

Our n-channel Si transistors are operated near pinch-off, where the channel size is roughly 300 nm wide \times 240 nm long. The signature of a single trap state is shown in the RTS source/drain current of Fig. 2a. Superimposed on the monotonically increasing background source/drain current is stochastic switching between two discrete values of channel current. This switching is the wellknown RTS (for a comprehensive review, see ref. 12) that is a hallmark of the capture and emission of one electron by a single trap state. The well-defined RTS evolution in Fig. 2a demonstrates that over the 690 to 740 mV range, the trap is energetically well isolated from other traps.

A filled trap implies electrostatic repulsion that diminishes the channel current. At high gate voltages (near point A in Fig. 2a), the Fermi level $E_{\rm F}$ is well above the trap level $E_{\rm T}$. Thus the trap is almost always filled, repelling electrons and allowing less current to flow in the source/drain channel. In contrast, at low gate voltages (near point C in Fig. 2a) when $E_{\rm F}$ is well below $E_{\rm T}$ the trap is empty most of the time and the high current state is more probable. At the midpoint, when $E_{\rm F} \approx E_{\rm T}$ (near point B in Fig. 2a) the probability of the trap filling is about 50%. Thus the source/drain current senses the charge state of the trap.

The trap studied here is a very stable defect because the behaviour is reproducible over many thermal cycles from room temperature to liquid He³ temperatures. From the signal-to-noise ratio, we find that the charge sensitivity of the our data acquisition system is $\sim 10^{-4} \, e \, \text{Hz}^{-1/2}$.

The Zeeman shift of the single trap¹³ can readily be identified by studying the trap energy shift of the point at which the probability of the trap filling is 50% (where $E_{\rm F}$ lines up with the trap energy $E_{\rm T}$), as a function of magnetic field. Figure 2b shows the Zeeman shift of this point as a function of an in-plane magnetic field. The trap energy shift was inferred from the gate voltage shift by using the procedure in ref. 13.

On the basis of the sign of the Zeeman shift, ref. 13 shows that the charging transition transfers from a single-charge state $1e^{-}$ to a double-charge state $2e^-$; that is, the charging is $1 \rightarrow 2$ rather than $0 \rightarrow 1$. In the energy diagram of Fig. 1b, the empty trap is modelled as an unpaired electron (for example, a dangling bond) that occupies the level $E_{\rm T}$ (the central red dashed line). In the presence of the magnetic field B, the single-electron state undergoes Zeeman splitting, indicated by the two solid lines at energies $E_{\rm T} \pm (1/2)E_{Z}$. At low temperatures and high fields, only the lower spin state is occupied. If $E_{\rm F}$ is raised, an additional electron from the channel can tunnel into the upper spin state in Fig. 1b, forming a two-electron singlet state (for example, a lone pair). Thus the Fermi energy required for forming the two-electron state would increase when B is increased, as suggested by Fig. 2b. In contrast, an initially 'spinless' empty trap would fill the lower Zeeman level, producing the opposite field dependence-that is, the required Fermi energy would decrease with increasing B, contrary to observation. Therefore the initial empty trap begins in a $1e^{-}$ paramagnetic state (S = 1/2) (high current state) while the filled trap (lower current state) is a $2e^{-}$ singlet state.

Our electron spin resonance (ESR) detection scheme is based on the changing balance between the two source/drain current states of the transistor, when the Larmor precession frequency produces spin flips. In effect, this is transistor-current-detected ESR, as illustrated in Fig. 1c. When the paramagnetic spin flips, the lower Zeeman level becomes available for trapping an additional electron. The trapping event diminishes the average source/drain current. A rate equation analysis of this trap/channel configuration gave¹⁴ the ESR-induced change in trap-filling probability.

To detect the ESR-microwave-induced change, we measured channel current at a fixed microwave frequency for 300 ms, during which there are about a few thousand RTS switching events, yielding good statistics for the average source/drain current. Figure 3a represents a fragment of such a trace over a 1 ms time interval. To complete the current versus magnetic field dependence, full 300-ms traces are taken at 150–250 different magnetic fields.



Figure 1 The mechanism for spin-to-charge conversion. **a**, A single paramagnetic trap, situated in a standard commercial n-channel Si FET that constitutes our sample. **b**, The Zeeman-split trap level relative to the FET channel Fermi level. The Fermi level would have to shift towards the upper Zeeman level to reach 50% occupation probability. (The singly

occupied state should actually be down-shifted by the Coulomb correlation energy U, not shown.) **c**, If the spin flips under ESR, the lower Zeeman level can become filled, producing the doubly occupied trap.

Because the signals are sometimes noisy, a systematic statistical procedure was used to measure the trap charge state. A histogram of the source/drain current data versus time was plotted as shown in Fig. 3c. In the histogram, the empty and filled trap states are represented by the large and small peak, respectively. For the perfect case of two discrete states, we expect two narrow peaks in the histogram. The broadening of the peaks in Fig. 3c is caused by noise. The charge trapping probability ratio is proportional to the area ratio of the two peaks. For certain traps, whose charge produces only a small change in source/drain channel current, an additional step was taken to avoid noise errors. A sophisticated algorithm¹⁵, for detection of abrupt step changes, was executed numerically. Figure 3a is the raw RTS, containing noise, whereas Fig. 3b shows the noiseless two-state switching, reconstructed by the algorithm.

We now present the ESR detection results for the single paramagnetic trap. Figure 4a shows the change in average source/drain current induced by a microwave frequency of 45.1 GHz. In Fig. 4a, an ESR peak in average current is centred around 16,025 G. Averaging blocks of four adjacent magnetic fields, the signal/noise ratio is >4:1, and the ESR feature is reproducible in different runs, and for different traps, in different samples.

We find that the ESR signal is most pronounced in the range of gate voltages corresponding to a paramagnetic (nearly empty) trap



Figure 2 Random telegraph signal senses the defect energy level. **a**, The channel current / was recorded over a narrow gate voltage ramp from 690–740 mV, swept in a 30-ms time interval, with a 1-mV source/drain bias voltage at 1.3 K. The discrete upper and lower current states sense the two charge states, which can monitor the two electron spin states. **b**, The positive Zeeman shift of the trap energy versus magnetic field implies a $1e^-$ to $2e^-$ transition in the defect, rather than a $0e^-$ to $1e^-$ transition. The error bars indicate the standard deviation in the raw data.

(that is, between points B and C in Fig. 2a). This is consistent with our assignment of filled and empty trap states.

The ESR signature is only found at temperatures below about 1 K. At those temperatures, the electron magnetic moment is substantially polarized, and in any case, microwave heating limits the temperature. From the RTS Boltzmann occupation probability as a function of voltage¹³, we find that the effective temperature rises to about 1.5 K when a moderate microwave power of 0.1 mW is applied to the sample, even though the bath temperature still remains at about 0.4 K. The sensitivity of our time-averaged readout is thus limited by the microwave heating. From our signal-to-noise ratio, we estimate that our ESR signal could be read in a single shot, if the ESR were driven by a pulsed 180-GHz source. In the future, heating can be avoided by using *g*-tensor modulation^{16,17} for single spin rotation.

Similar runs have been carried out at other frequencies and in various samples. The ESR magnetic resonance as a function of microwave frequency is plotted in Fig. 4b. For $hf = g\mu_B B$, a *g*-factor of 2.02 \pm 0.015 is obtained. For a *g*-factor reference we used both a bulk Si crystal doped with 2×10^{18} cm⁻³ phosphorous; and the channel electrons in our n-channel Si transistor (Fig. 1a). Both agreed with the well-established values of $g \approx 1.998$. Because conduction electrons always¹⁸ have g < 2, and paramagnetic centres in SiO₂ always⁷ have g > 2, our results indicate a paramagnetic centre in the oxide, or at the SiO₂/Si interface. Our observed *g*-value is somewhat larger than that for some known paramagnetic centres near the Si/SiO₂ interface. A P_b paramagnetic centre is known to



Figure 3 Detailed analysis of the random telegraph signal. **a**, The raw random telegraph data displayed for a time interval of 1 ms. **b**, An algorithm for detection of abrupt changes is applied to the raw data to reconstruct the two RTS levels. This procedure reduces the statistical errors due to noise. **c**, A histogram of the time-domain data. The large and small peaks represent the empty and filled trap states, respectively.

have a *g*-factor of 2.006 along the $\langle 100 \rangle$ direction, whereas the E' centre is expected to have g = 2.001. One possibility is that we are looking at a centre that has a local structure different from that of these two typical examples. Another possibility is that the low-

Figure 4 The single electron ESR signal. **a**, The single-occupancy trap probability, and the average source/drain current, versus magnetic field for a fixed microwave frequency. The peak represents electron spin resonance. The error bars (about 1%) indicate the standard deviation in a 300-ms data set averaged over four adjacent magnetic fields. **b**, The linear relationship of resonant magnetic field versus microwave frequency indicates a *g*-factor of 2.02 \pm 0.015.

Number of jumps

density conduction channel electrons might have slight ferromagnetic ordering, giving rise to a local field that slightly increases the apparent *g*-factor of the trap.

The full-width, half maximum linewidth of the observed resonance is about 5–10 G, which corresponds to the spin-spin relaxation time T_2 of about 0.1 µs. This time is much less than the typical 100-µs lifetime for isolated paramagnetic spins (the paramagnetic E' centre in irradiated fused quartz has a T_2 of about 25 µs at room temperature; see for example, ref. 19) in SiO₂. There might be additional decoherence caused by the conduction electrons in the channel, or it might be caused by power broadening. Indeed, our microwave power produces a Rabi frequency $\gamma H_1 \approx 10$ kHz, comparable to the trap tunnelling time of about 20 kHz, where γ is the gyromagnetic ratio and H_1 is the microwave *B* field. For actual quantum information processing, the source/drain channel should only be un-pinched at the end of a quantum computation for readout, well within the T_1 lifetime.

At the strong Rabi frequency of $\gamma H_1 \approx 10$ kHz that is present at the end plate of our rectangular waveguide, nonlinear effects emerge. At lower microwave powers $\gamma H_1 \approx 1$ kHz, the singleoccupancy trap probability is correctly observed to diminish under ESR, as expected from the reasoning in Fig. 1c. However at higher powers, $\gamma H_1 \approx 10$ kHz, the ESR-induced-current signal inverts, leading to an increase in occupancy trap probability, as plotted in Fig. 4a. We plotted the signal at high microwave power because it has a better signal-to-noise ratio.

We also see a nonlinear ESR response in the tunnelling dynamics. As shown in Fig. 5, the frequency of RTS jumps, which is proportional to the tunnelling rate, can change as much as 10% at the ESR condition. At low microwave power, $\gamma H_1 \approx 1$ kHz, the tunnelling rate increases at resonance, but at high Rabi frequency, $\gamma H_1 \approx 10$ kHz, the tunnelling rate inverts, as shown in Fig. 5. These inverted signals in both the source/drain current, and the tunnelling frequency, are only observed for high Rabi frequencies that are comparable to the tunnelling frequency. The nonlinear ESR response may be partly associated with non-resonant microwave-induced changes in the tunnelling rate, but we have not yet identified the dominant nonlinear mechanism.

Our evidence in support of the ESR signal arising from a single electron is summarized as follows: (1) The ESR signature is only observed in the random telegraph signal crossover region, predominantly between points B and C of Fig. 2a. (2) The random telegraph signal switching statistics, and thermal occupation probabilities, correspond to those of a single defect. (3) The *g*-factor value rules out channel electrons. The experiment demonstrates that an FET channel can effectively monitor the magnetic resonance of an adjacent single spin.



function of field for three different microwave frequencies. The resonance positions are marked by the vertical arrows. The error bars represent the standard deviation in the given data set.





Received 5 April; accepted 7 June 2004; doi:10.1038/nature02727

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Acknowledgements We would like to thank P. M. Lenahan for information regarding the relaxation of spin centres in SiO₂, and K. Wang for helping to secure the samples. The work was supported by the Defense Advanced Research Projects Agency and the Defense MicroElectronics Activity.

Competing interests statement The authors declare that they have no competing financial interests.

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Surface transfer doping of diamond

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The electronic properties of many materials can be controlled by introducing appropriate impurities into the bulk crystal lattice in a process known as doping. In this way, diamond (a well-known insulator) can be transformed into a semiconductor¹, and recent progress in thin-film diamond synthesis has sparked interest in the potential applications of semiconducting diamond^{2,3}. However, the high dopant activation energies (in excess of 0.36 eV) and the limitation of donor incorporation to (111) growth facets only have hampered the development of diamond-based devices. Here we report a doping mechanism for diamond, using a method that does not require the introduction of foreign atoms into the diamond lattice. Instead, C₆₀ molecules are evaporated onto the hydrogen-terminated diamond surface, where they induce a subsurface hole accumulation and a significant rise in two-dimensional conductivity. Our observations bear a resemblance to the so-called surface conductivity of diamond⁴⁻⁸ seen when hydrogenated diamond surfaces are exposed to air, and support an electrochemical model in which the reduction of hydrated protons in an aqueous surface layer gives rise to a hole accumulation layer⁶⁷. We expect that transfer doping by C_{60} will open a broad vista of possible semiconductor applications for diamond.

The C₆₀ was evaporated onto differently prepared diamond surfaces and onto a silica glass substrate. After each evaporation step the conductance of the respective sample was measured as described below and without breaking the vacuum. The closed symbols in Fig. 1 refer to the conductance of two different homoepitaxial diamond samples with hydrogenated surfaces as a function of C₆₀ coverage in a double-logarithmic plot. The starting point is in both cases the annealed state (see Methods) with a conductance well below 10^{-12} S (leftmost data point). With increasing C₆₀ coverage the conductance rises rapidly by more than six orders of magnitude and saturates at 10^{-6} S for sample D20 and 10^{-5} S for sample D34 at a C₆₀ coverage between 4 and 8 monolayers (ML). Two control experiments (open symbols) were performed with silica glass and with diamond sample D20, this time with an oxidized instead of a hydrogenated surface as the substrate for C_{60} evaporation. The negative results presented in Fig. 1 confirm that the conductivity necessarily requires the combination of C₆₀ and hydrogenated diamond; the C₆₀ layer alone on an insulating substrate (glass) or in combination with an oxidized diamond surface is not sufficient.

We propose that the cause of the observed conductivity is a transfer doping mechanism whereby electrons are transferred from diamond to the C60 layer such that an equal amount of electrons and holes reside on the C60 and diamond side of the interface, respectively. For this to work, the energy level scheme as sketched in Fig. 2a has to be considered. The left-hand part depicts the valence and conduction band edges (VBM and CBM) of diamond, together with the Fermi level E_F. On the right-hand side a limited number of occupied and unoccupied molecular orbitals of C₆₀ are schematically sketched with the highest occupied (HOMO) and the lowest unoccupied (LUMO) bracketing the fundamental gap of C₆₀. The common reference level is the vacuum level E_{VAC}. For hydrogenterminated diamond the electron affinity is negative, that is, the CBM lies 1.3 eV above E_{VAC} (refs 9, 10). The electron affinity $E_{\rm VAC} - E_{\rm LUMO}$ of C₆₀ is measured to be about 2.7 eV for the C₆₀ molecule¹¹, and this places the Fermi level of C₆₀, which lies between the HOMO and the LUMO, well below the $E_{\rm F}$ of diamond. Hence,





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